

Fig 1A Prior Art

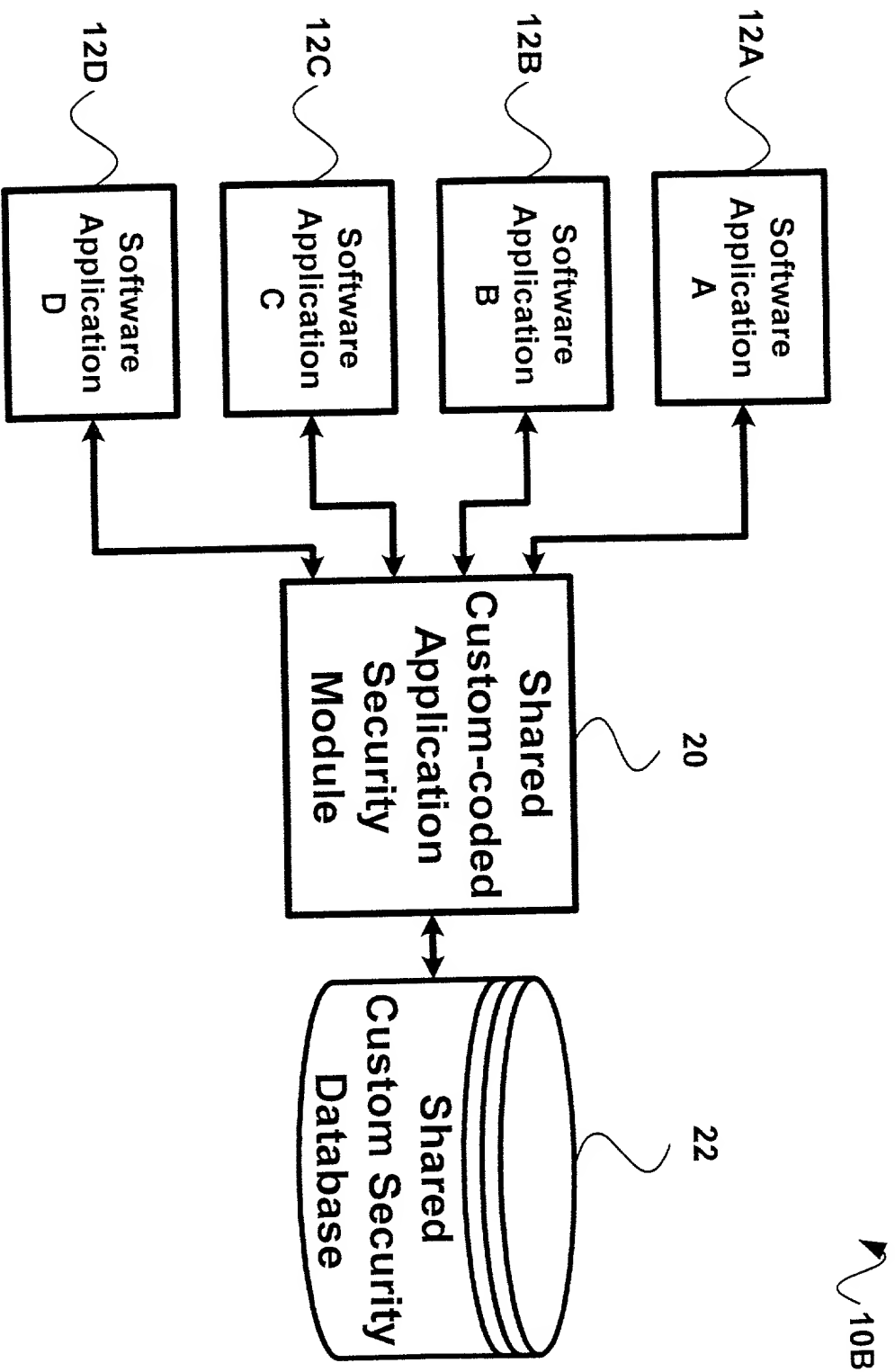


Fig 1B Prior Art

10C

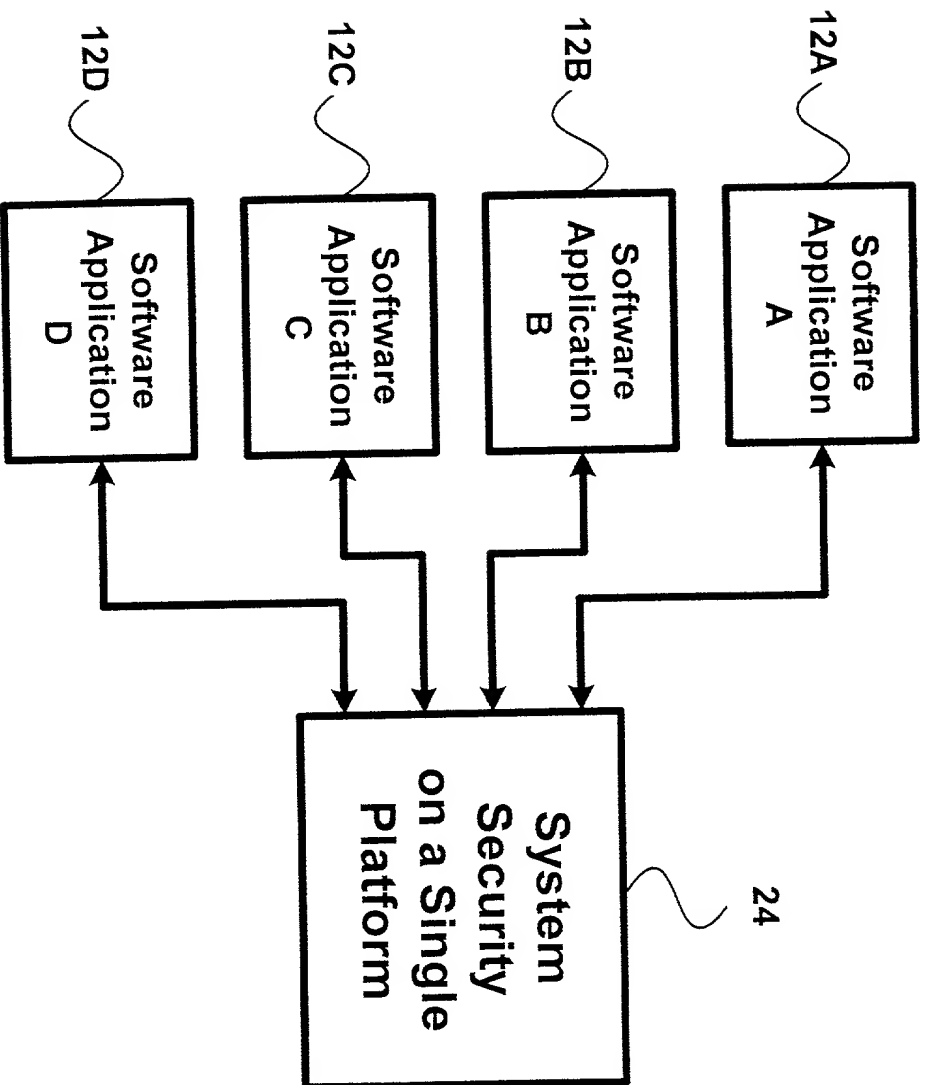


Fig 1C Prior Art

10D

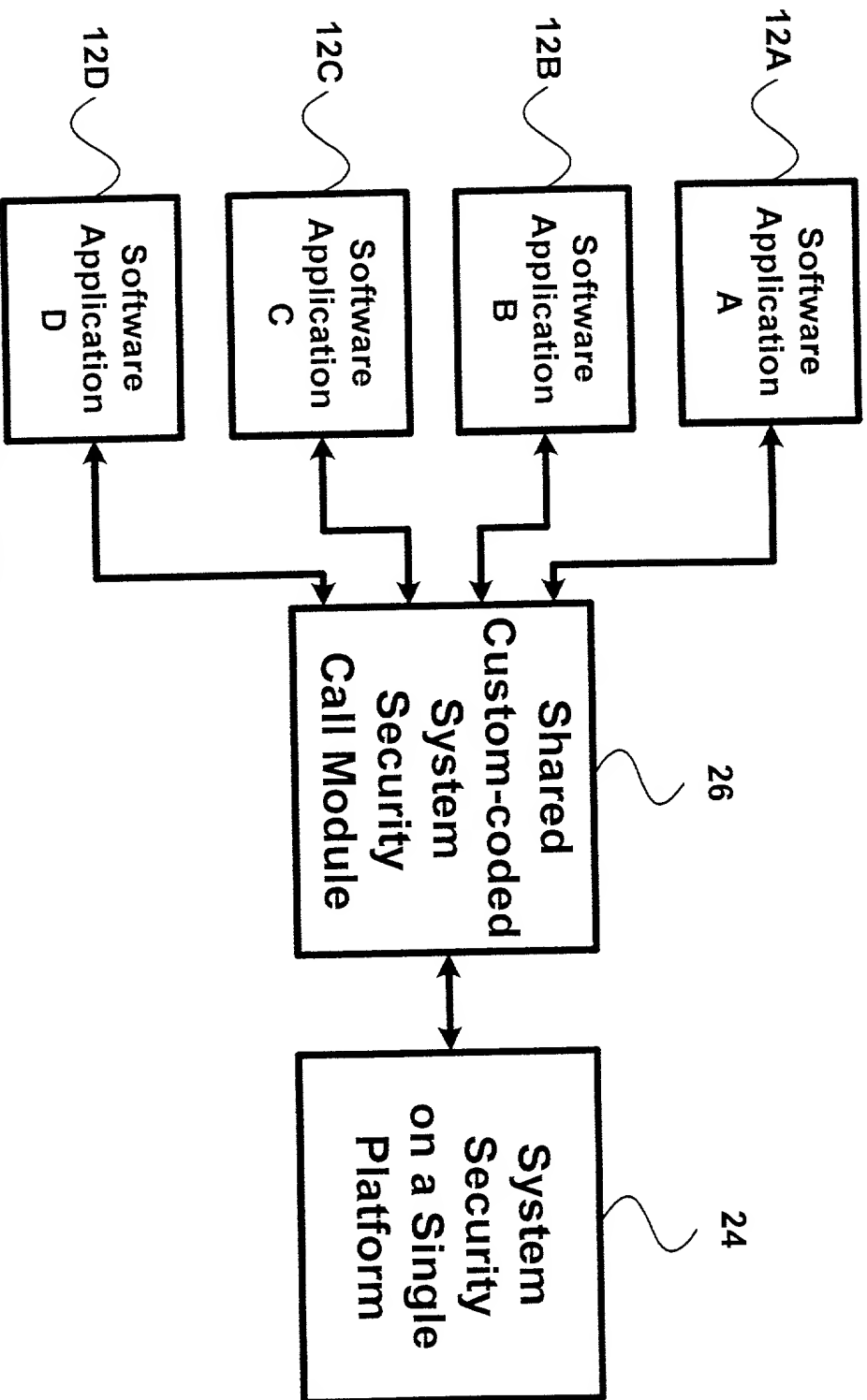


Fig 1D Prior Art

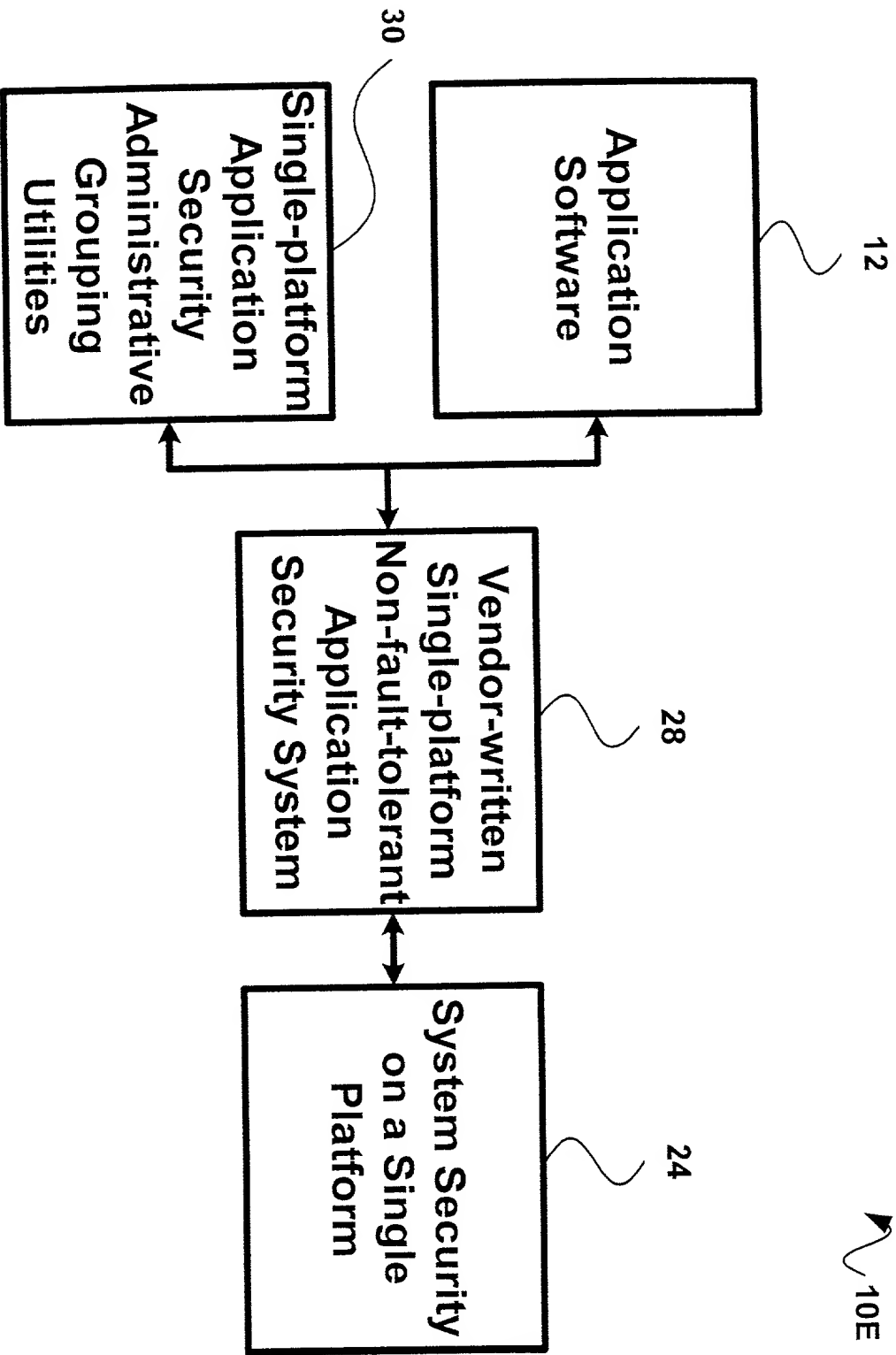


Fig 1E Prior Art

FIG. 1E is a block diagram of a prior art system architecture. The system includes an application software block (12) which is connected to a single-platform application security administrative grouping utilities block (30). The single-platform application security administrative grouping utilities block (30) is connected to a vendor-written single-platform non-fault-tolerant application security system block (28). The vendor-written single-platform non-fault-tolerant application security system block (28) is connected to a system security on a single platform block (24) via a bidirectional arrow. A reference numeral 10E is shown at the bottom right of the diagram.

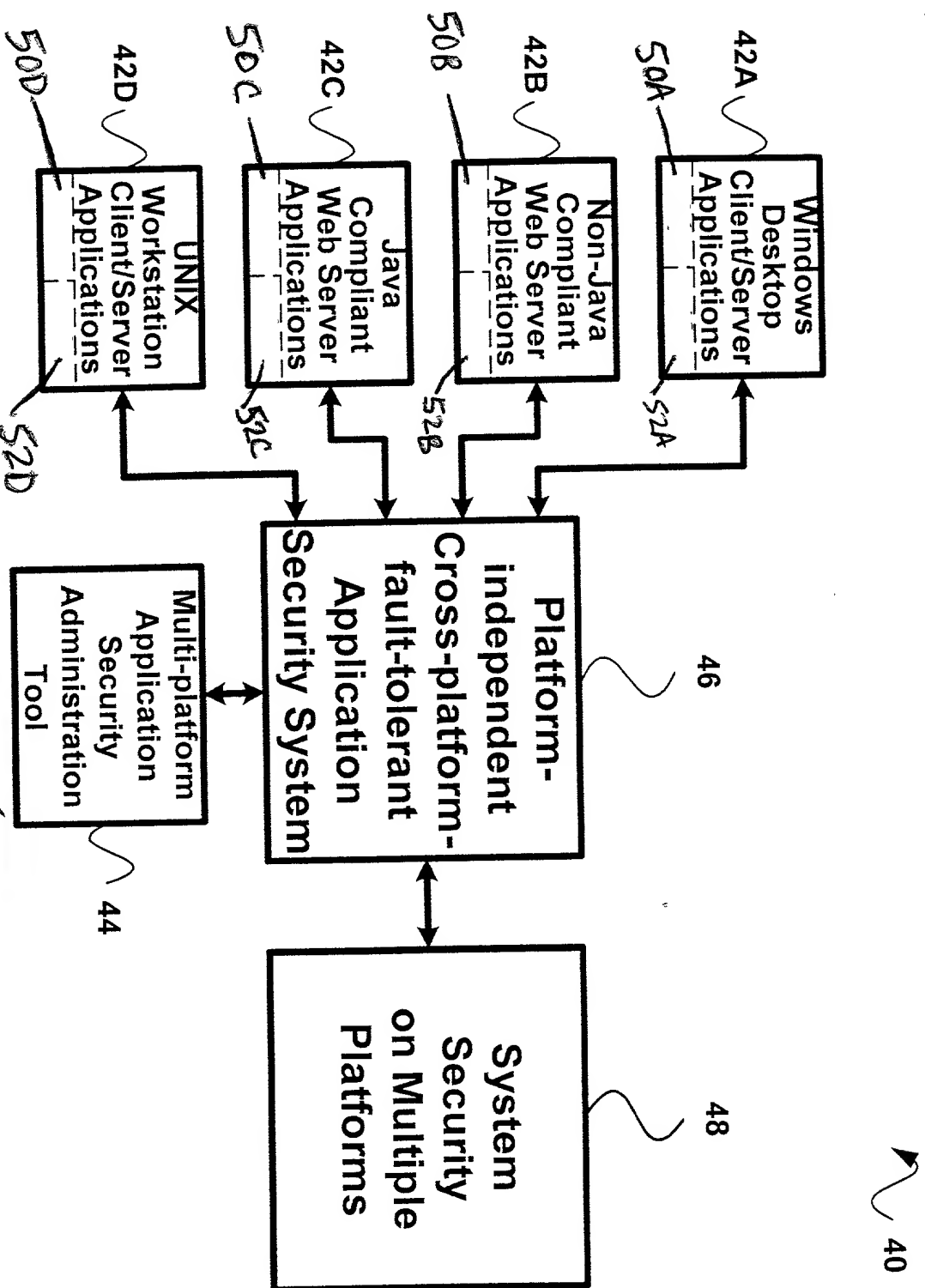


Fig 2

FIG. 2 is a block diagram of a security system architecture. The system includes a central security system (46) that receives input from multiple client applications (42A, 42B, 42C, 42D) and provides security services to a multi-platform application security administration tool (44) and a system security on multiple platforms (48).

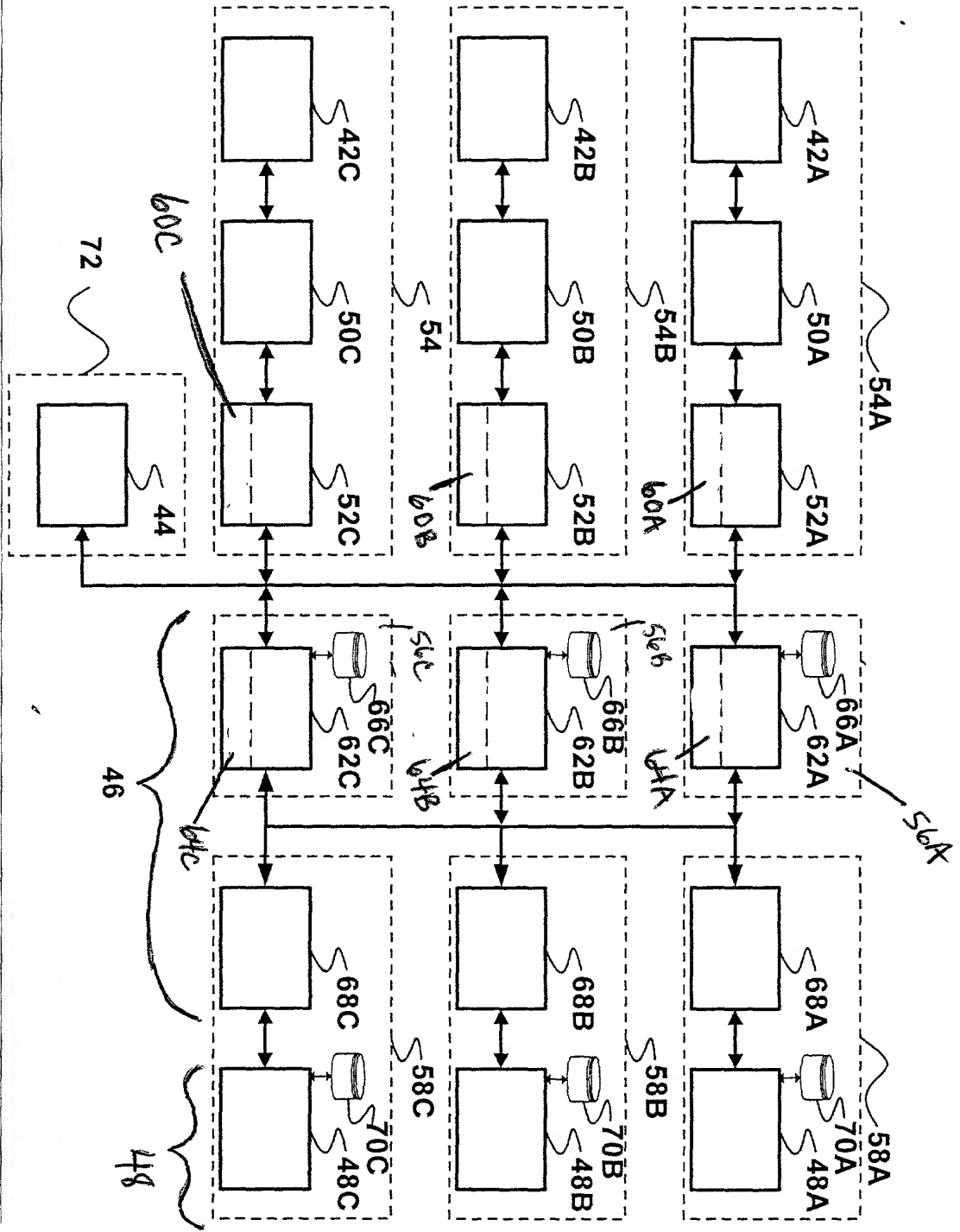


Fig 3

FIG. 3 is a block diagram of a system architecture. The system architecture includes a central bus (46) and three parallel processing paths (54A, 54B, 54C). Each path includes a sequence of blocks (42A-C, 50A-C, 52A-C) and a storage unit (66A-C, 68A-C, 70A-C). A control unit (44) is connected to the bus. A feedback loop (72) connects the output of the third path back to the input of the first path.

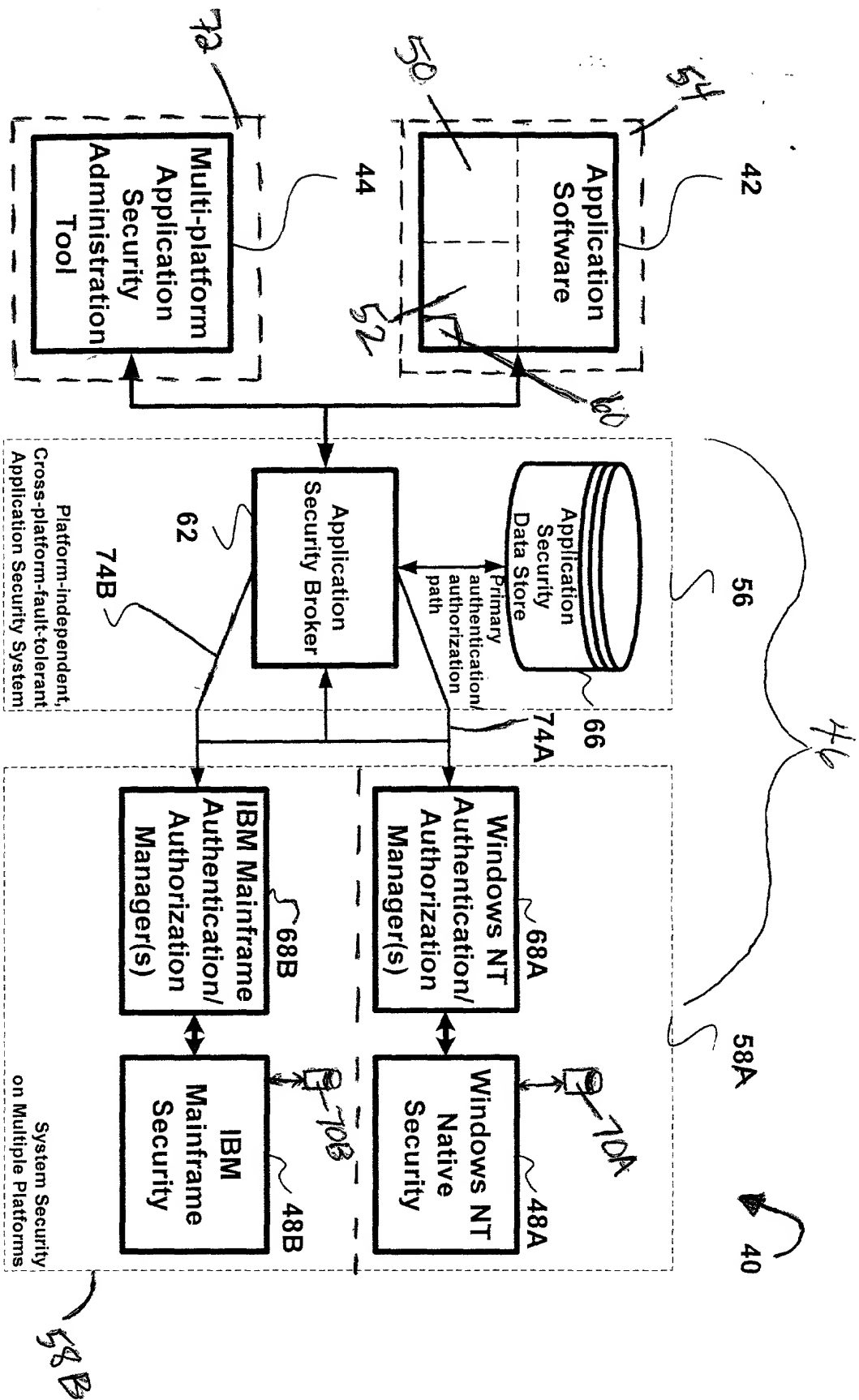


Fig 4

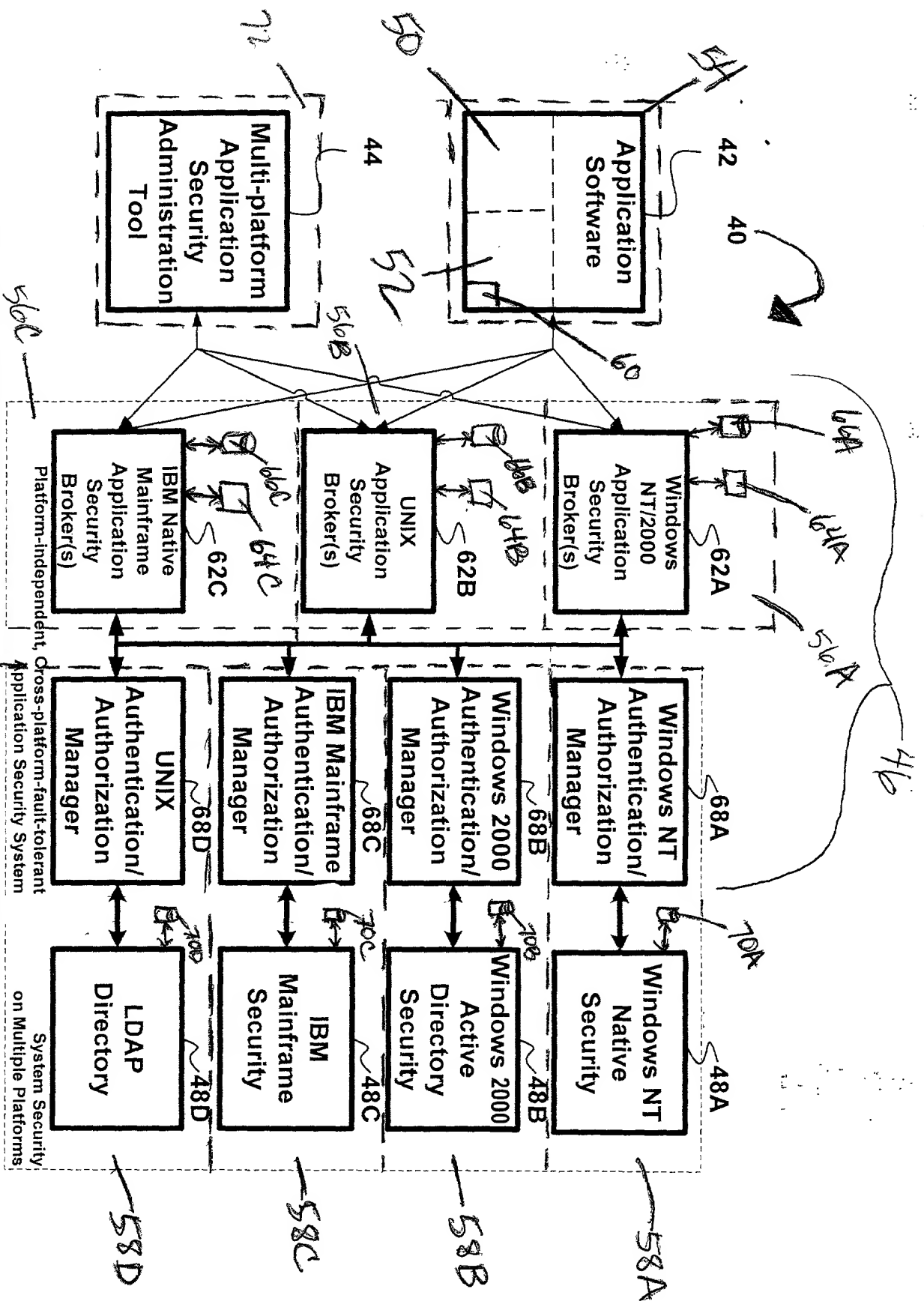


Fig 5